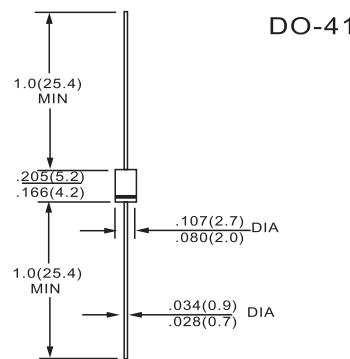


**FEATURES**

- Low profile, axial leaded outline
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Very low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability



Dimensions in inches and (millimeters)

**MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS**
**VOLTAGE RATINGS**

PARAMETER	SYMBOL	11DQ09	11DQ10	UNITS
Maximum DC reverse voltage	$V_R$			
Maximum working peak reverse voltage	$V_{RWM}$	90	100	V

**ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current See fig. 4	$I_{F(AV)}$	50 % duty cycle at $T_C = 75^\circ\text{C}$ , rectangular waveform	1.1	A
Maximum peak one cycle non-repetitive surge current See fig. 6	$I_{FSM}$	5 $\mu\text{s}$ sine or 3 $\mu\text{s}$ rect. pulse	85	
		10 ms sine or 6 ms rect. pulse	14	
Non-repetitive avalanche energy	$E_{AS}$	$T_J = 25^\circ\text{C}$ , $I_{AS} = 0.5 \text{ A}$ , $L = 8 \text{ mH}$		mJ
Repetitive avalanche current	$I_{AR}$	Current decaying linearly to zero in 1 $\mu\text{s}$ Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_R$ typical		A

**ELECTRICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum forward voltage drop See fig. 1	$V_{FM}^{(1)}$	1 A	0.85	V
		2 A	0.96	
		1 A	0.68	
		2 A	0.78	
Maximum reverse leakage current See fig. 2	$I_{RM}^{(1)}$	$T_J = 25^\circ\text{C}$	0.5	mA
		$T_J = 125^\circ\text{C}$	1.0	
Typical junction capacitance	$C_T$	$V_R = 5 \text{ V}_{\text{DC}}$ (test signal range 100 kHz to 1 MHz) $25^\circ\text{C}$	35	pF
Typical series inductance	$L_S$	Measured lead to lead 5 mm from package body	8.0	nH
Maximum voltage rate of change	dV/dt	Rated $V_R$	10 000	V/ $\mu\text{s}$

**THERMAL - MECHANICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	$T_J^{(1)}, T_{Stg}$		- 40 to 150	°C
Maximum thermal resistance, junction to ambient	$R_{thJA}$	DC operation Without cooling fin	100	°C/W
Typical thermal resistance, junction to lead	$R_{thJL}$	DC operation See fig. 4	81	
Approximate weight			0.33	g
			0.012	oz.
Marking device		Case style DO-204AL (DO-41)	11DQ09	
			11DQ10	

**Note**

$$(1) \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$$
 thermal runaway condition for a diode on its own heatsink



**VISHAY MAS**  
**GENERAL SEMICONDUCTOR**

**11DQ09 THRU 11DQ10**

90V , 100V 1.1A

## RATINGS AND CHARACTERISTIC CURVES 11DQ09 THRU 11DQ10

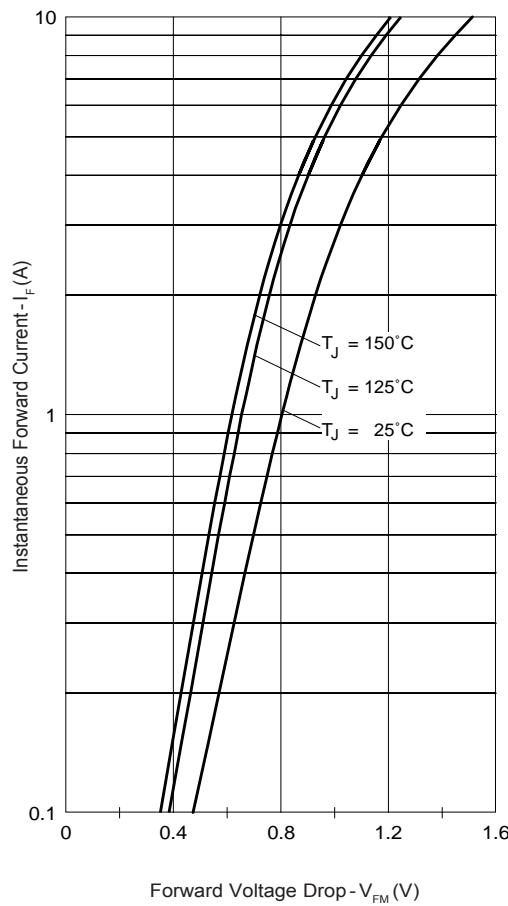


Fig. 1 - Max. Forward Voltage Drop Characteristics

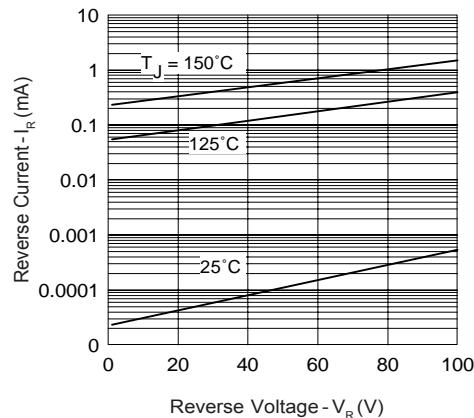


Fig. 2 - Typical Values Of Reverse Current Vs. Reverse Voltage

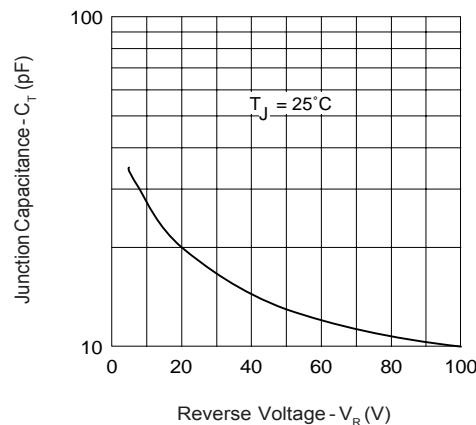


Fig. 3 - Typical Junction Capacitance Vs. Reverse Voltage

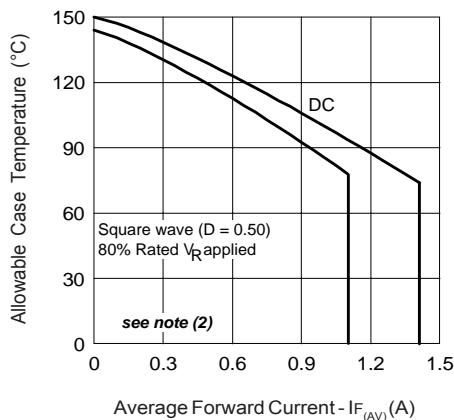


Fig. 4 - Max. Allowable Case Temperature Vs. Average Forward Current

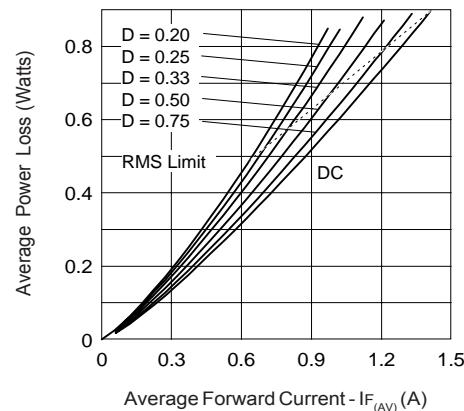


Fig. 5 - Forward Power Loss Characteristics

(2) Formula used:  $T_C = T_J - (P_d + P_{d,REV}) \times R_{thJC}$ ;  
 $P_d = \text{Forward Power Loss} = I_{F(AV)} \times V_{FM} @ (I_{F(AV)}/D)$  (see Fig. 6);  
 $P_{d,REV} = \text{Inverse Power Loss} = V_{R1} \times I_R(1-D); I_R @ V_{R1} = 80\% \text{ rated } V_R$